

848P-A

Rev :2.0 (P4 478P Processor with DDR SDRAM Mainboard)

PCB P/N: 15-H75-011010

BOM P/N: 89-200-H75000

Schematics Version History Table :

Circuit Ver.	PCB Ver.	Total Page	Modificatory Page	Date
A	A	32		5 /26 / ' 03
1.0a	1.0a	32		
2.0	2.0	32		

Page Title of Schematic :

Title	Page	Title	Page
Cover Sheet	1		
System Block Diagram	2		
P4 478P Part A	3		
P4 478P Part B	4		
P4 478P Part C	5		
Clock Generator	6		
I865(MCH)Part A	7		
I865(MCH)Part B	8		
DDR Serial Resistors	9		
DDIMM 1&2 (DDR SDRAMs)	10		
DDR SSTL-2 Terminations	11		
DDR Power	12		
AGP 8X Slot 1.5V	13		
ICH5 Part A	14		
ICH5 Part B	15		
ICH5 Part C	16		
IDE Connector	17		
USB/FWH	18		
LPC_FDD/KB/M	19		
I/O Ports	20		
H/W Monitor	21		
AC97 Codec	22		
Audio Interface	23		
ATX Power & Front Panel	24		
LAN	25		
Vcore DC-DC	26		
MIS DC-DC	27		
PCI Slot 1&2	28		
PCI Slot 3	29		
PCI Slot 4&5	30		
Back I/O	31		
VT6307 (1394)	32		

DEVICE	IDSEL	INT#	REQ#	GNT#
PCI1	16	C/D/E/F	PREQ-1	PGNT-1
PCI2	17	D/E/F/G	PREQ-2	PGNT-2
PCI3	18	E/F/G/H	PREQ-3	PGNT-3
PCI4	19	F/G/H/C	PREQ-4	PGNT-4
PCI5	20	G/H/C/D	X	X
LAN	25	F	PREQ-5	PGNT-5
1394	26	G	PREQ-0	PGNT-0

intel
P4 Processor

478 pin

BW : 3.2GB/s @ FSB : 400MHz & Freq : 100MHz
BW : 4.1GB/s @ FSB : 533MHz & Freq : 133MHz
BW : 6.4GB/s @ FSB : 800MHz & Freq : 200MHz

BW : 1.6GB/s @ DDR : 200/266MHz & Freq : 100MHz
BW : 2.1GB/s @ DDR : 200/266/333MHz & Freq : 133MHz
BW : 3.2GB/s @ DDR : 266/333/400MHz & Freq : 200MHz

BW : 1.066GB/s @ Freq : 66MHz

AGP1 Slot 132p
8X / 1.5V

VGA (G only)

intel
iSP/G

932pin FC-BGA

DDIMM1: DDR Socket 184P

DDIMM2 : DDR Socket 184P

DDIMM3: DDR Socket 184P

DDIMM4 : DDR Socket 184P

BW : 266MB/s @ Freq : 66MHz

USB V2.0

USB1
2 ports

USB2
2 ports

USB3
2 ports

USB4
2 ports

USBLAN
8 ports

IDE2 40pin
IDE1 40pin

Up to Ultra ATA/100
Two IDE Channel

intel
ICH5

460pin EPGA

BW : 133MB/s @ Freq : 33MHz

PCI1 Slot 120pin @ AD16

PCI2 Slot 120pin @ AD17

PCI3 Slot 120pin @ AD18

SATA1 7Pin
SATA2 7pin

AC' 97 & Lan I/F

Audio Codec
CMI9739

Mic In
Line Out
Line In

intel
FWH
32pin PLCC

LPC bus

Super I/O
W38637F
128pin PQFP

CONN/
HEADER

VIA 1394

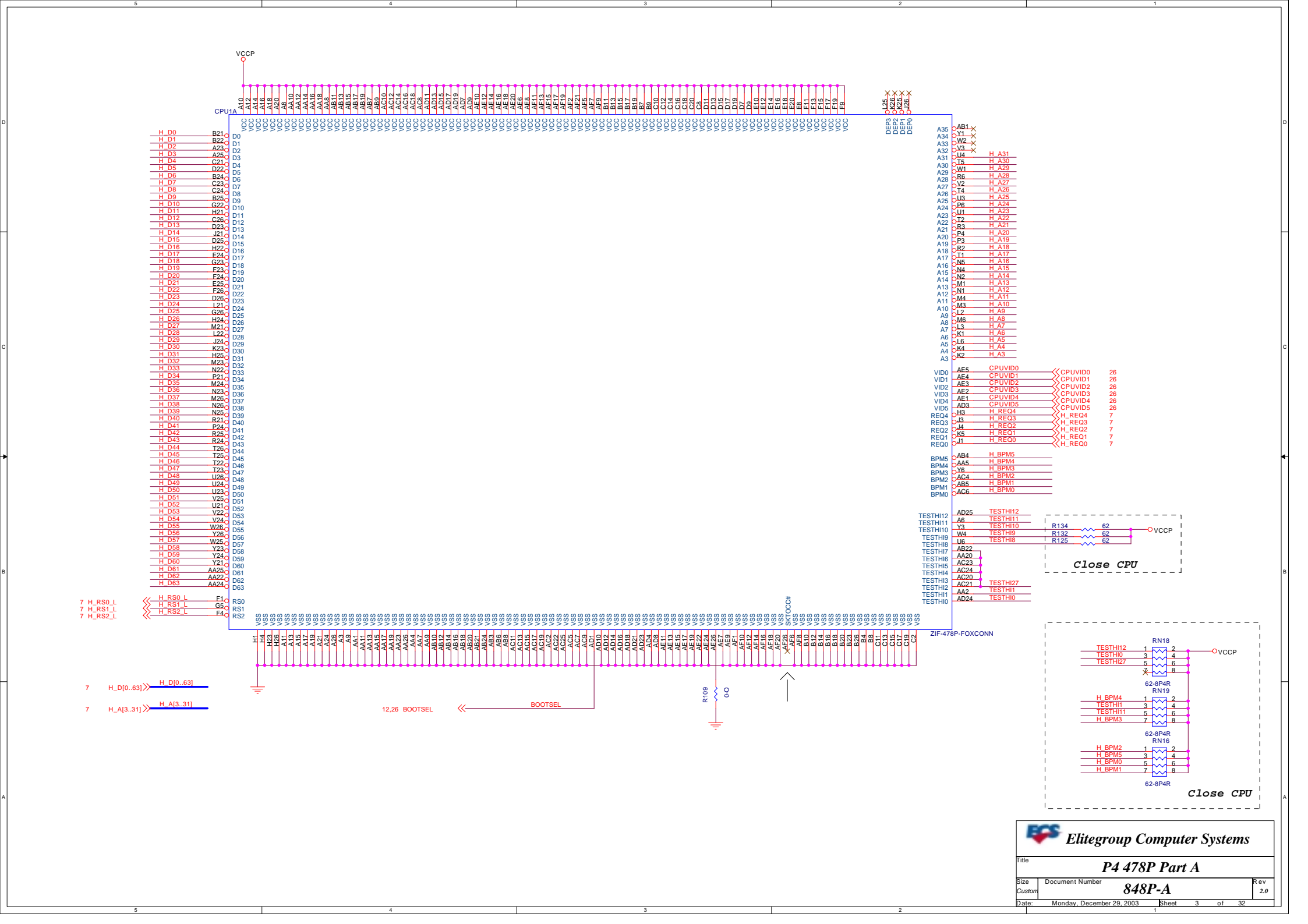
Giga Lan

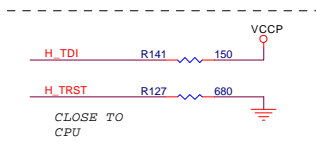
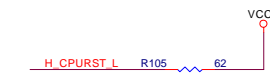
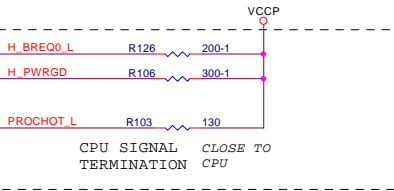
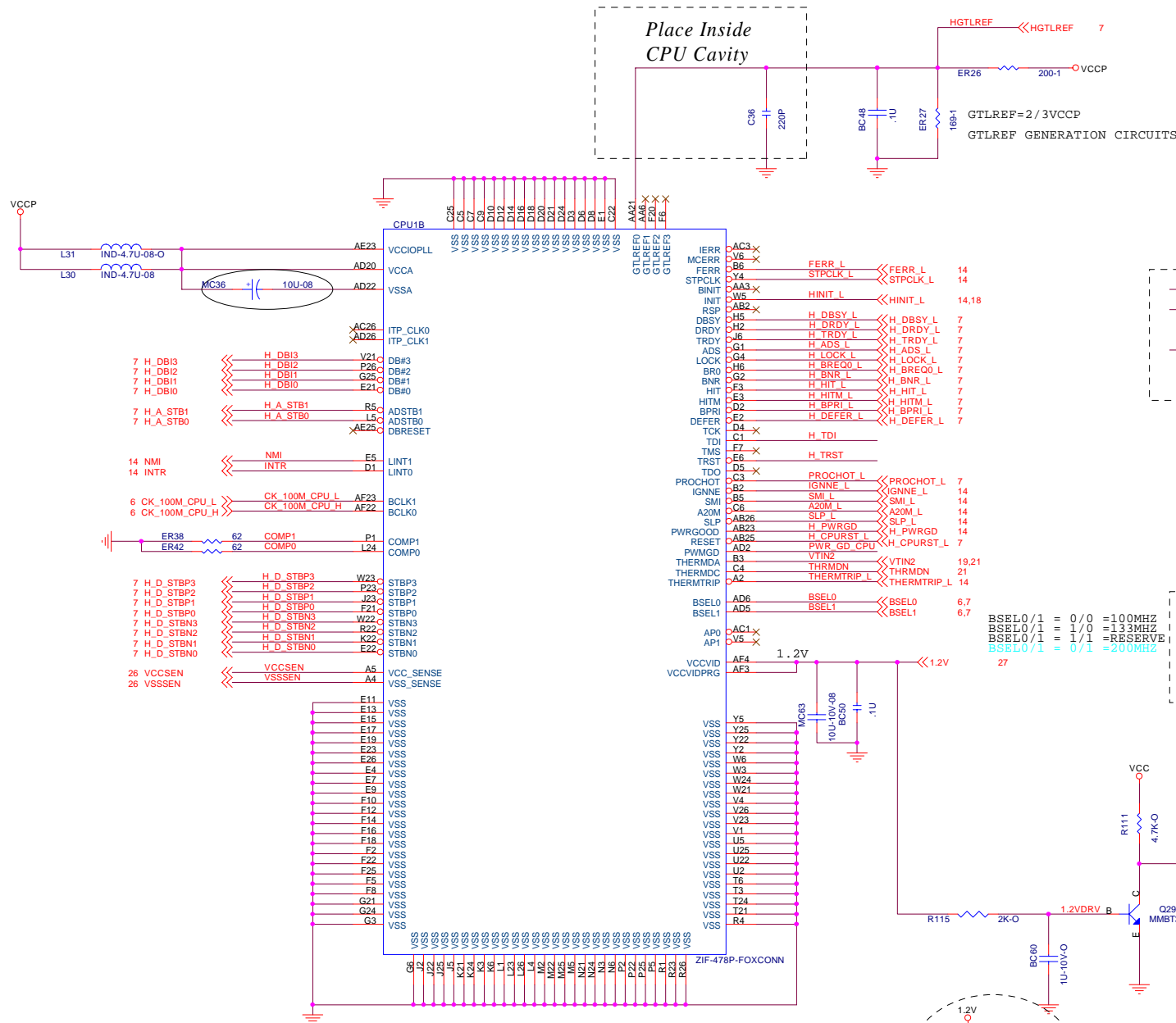
USBLAN
RJ45



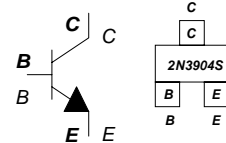
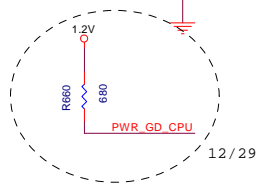
Elitegroup Computer Systems

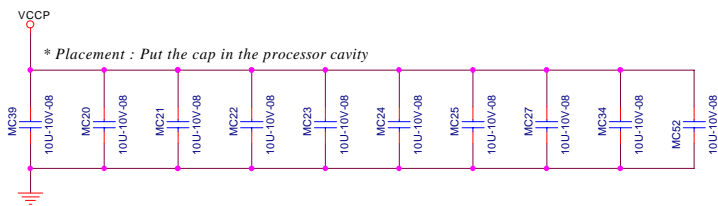
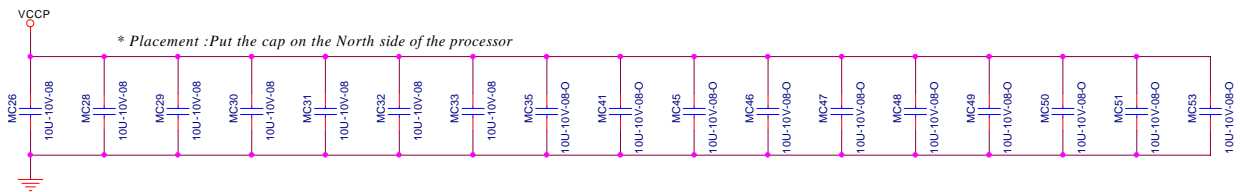
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System Block Diagram		
Size B	Document Number	Rev 2.0
848P-A		
Date:	Monday, December 29, 2003	Sheet 2 of 32

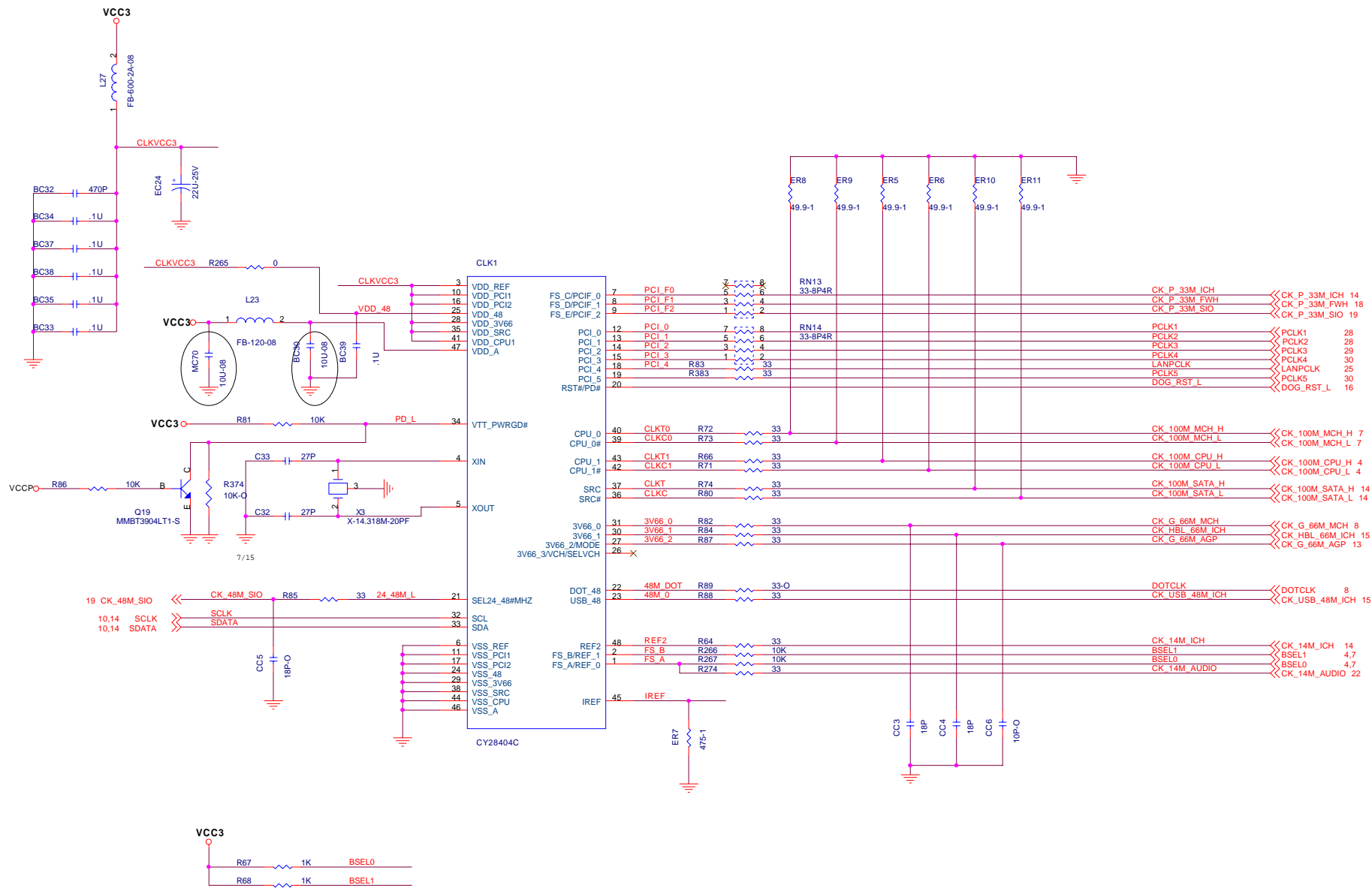


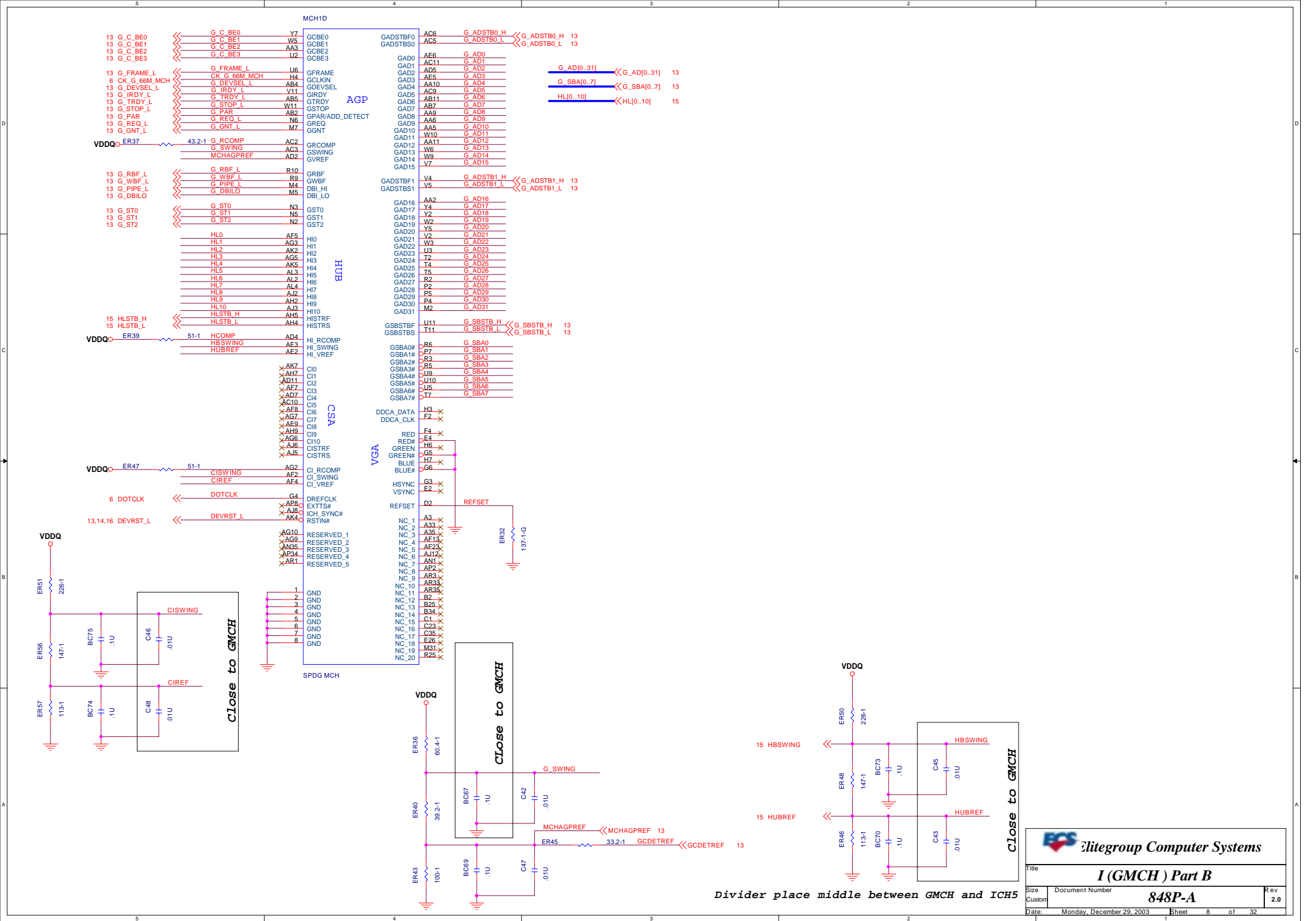


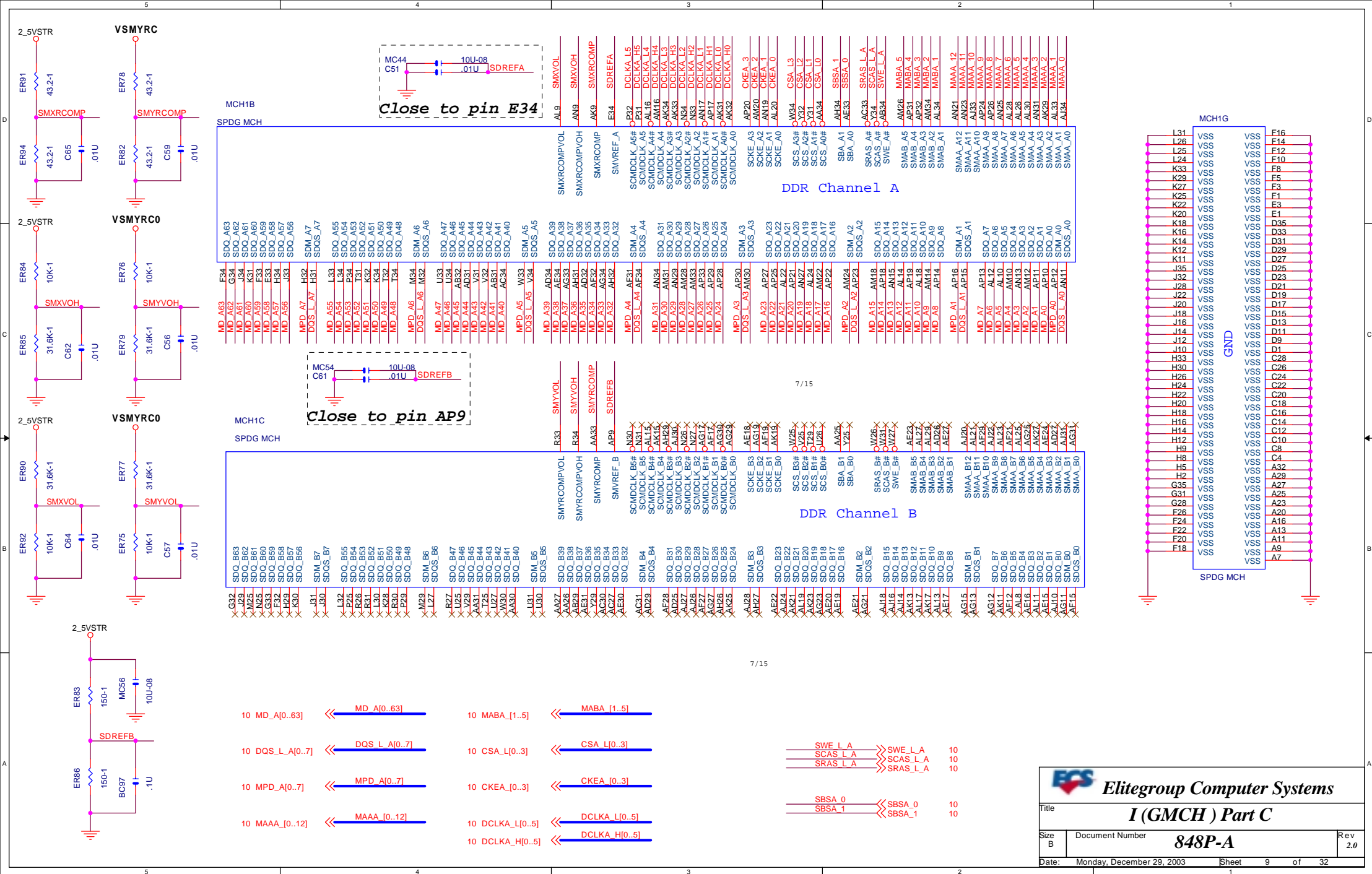
BSEL0/1 = 0/0 = 100MHZ
 BSEL0/1 = 1/0 = 133MHZ
 BSEL0/1 = 1/1 = RESERVE
 BSEL0/1 = 0/1 = 200MHZ

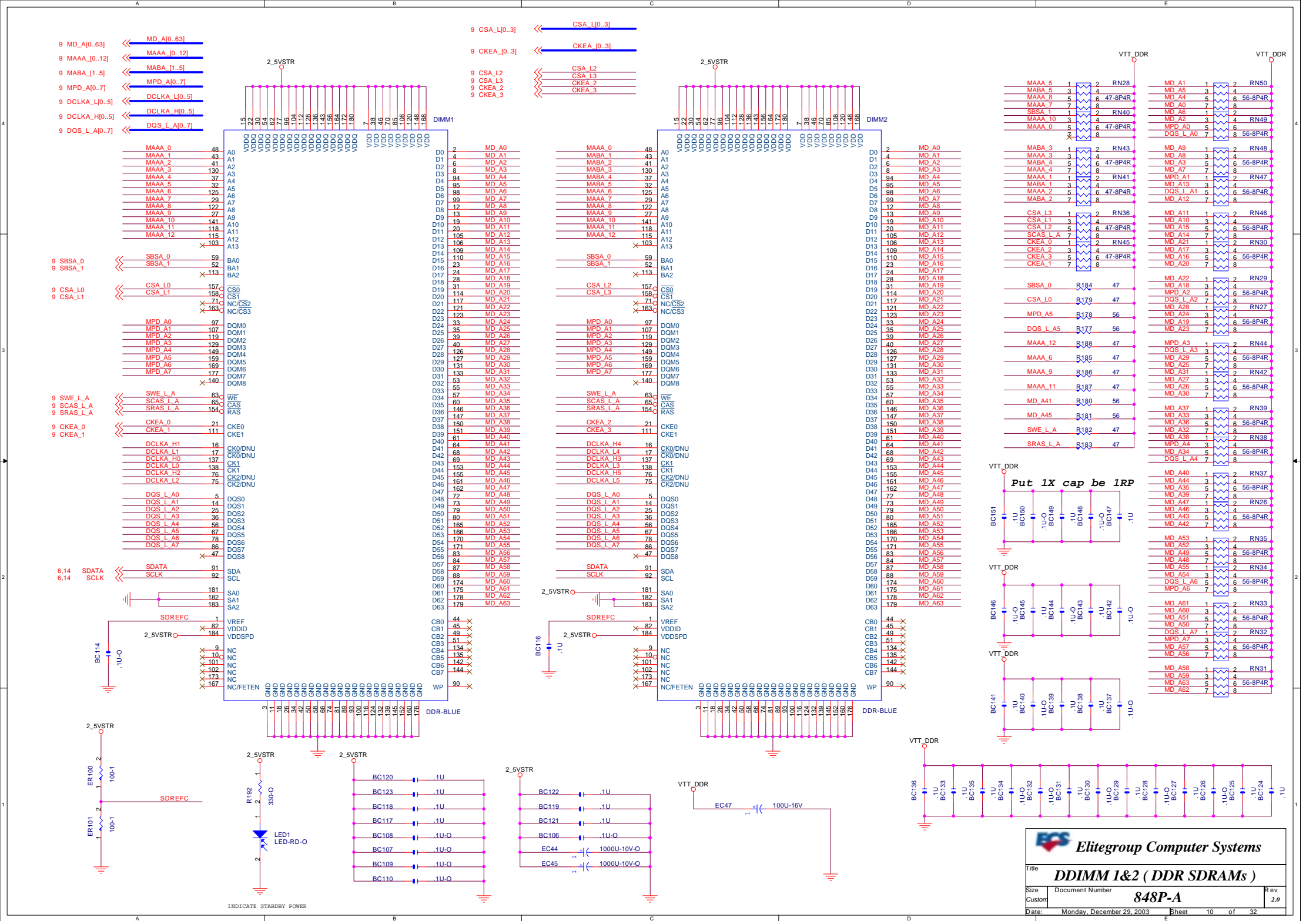


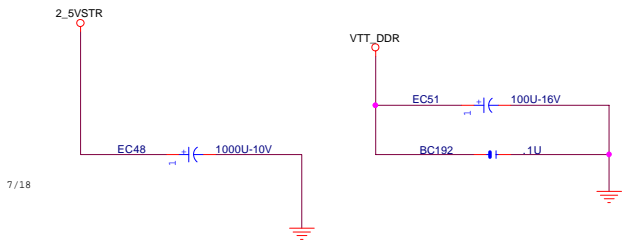







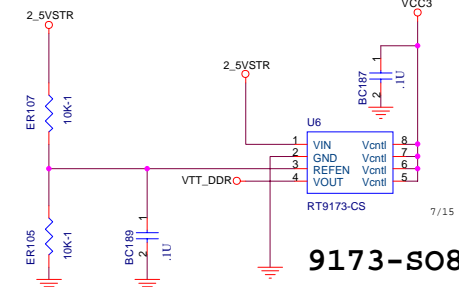
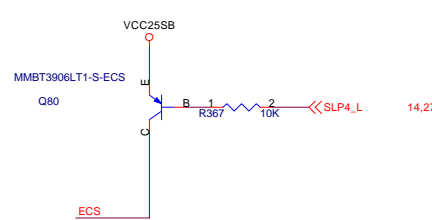
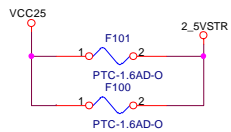
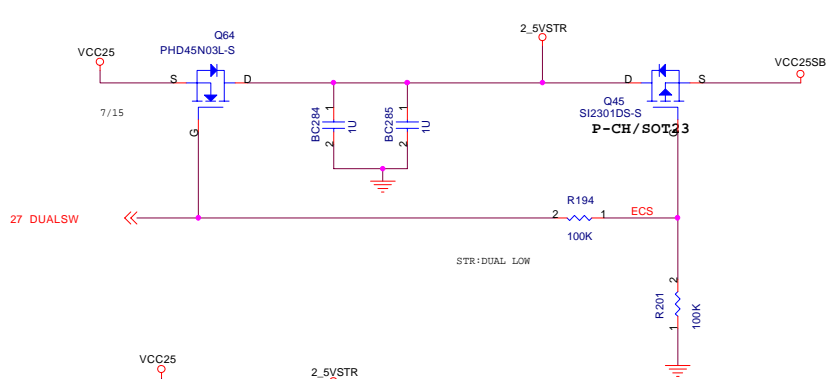




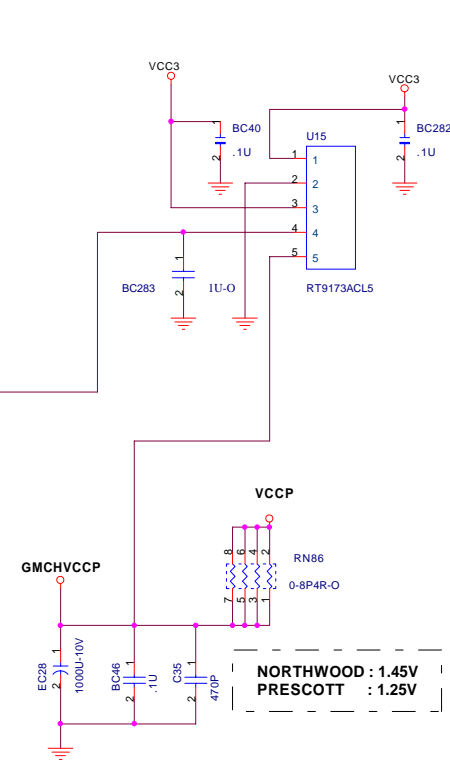
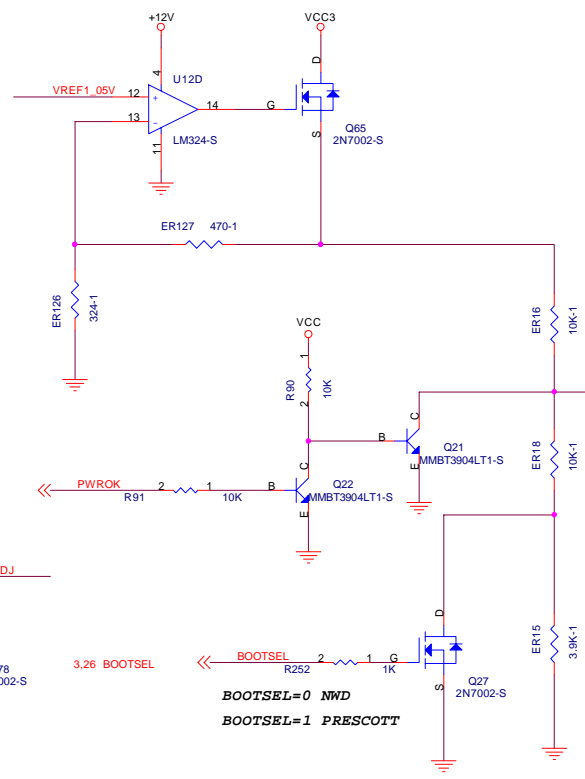
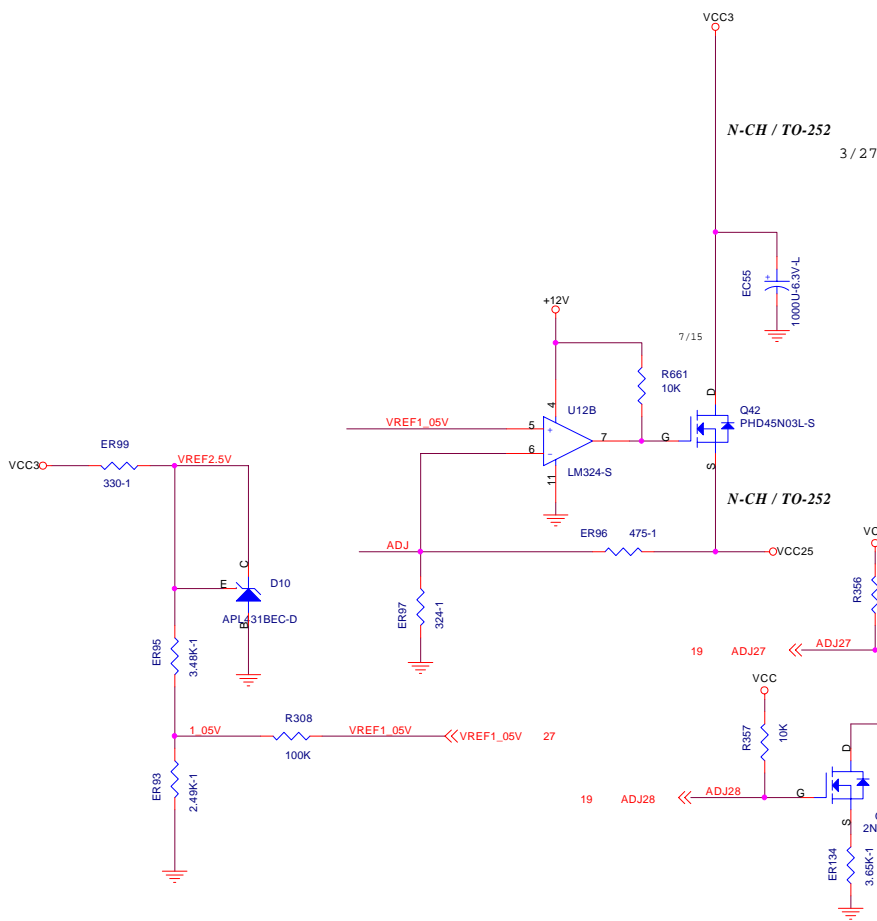


7 / 18

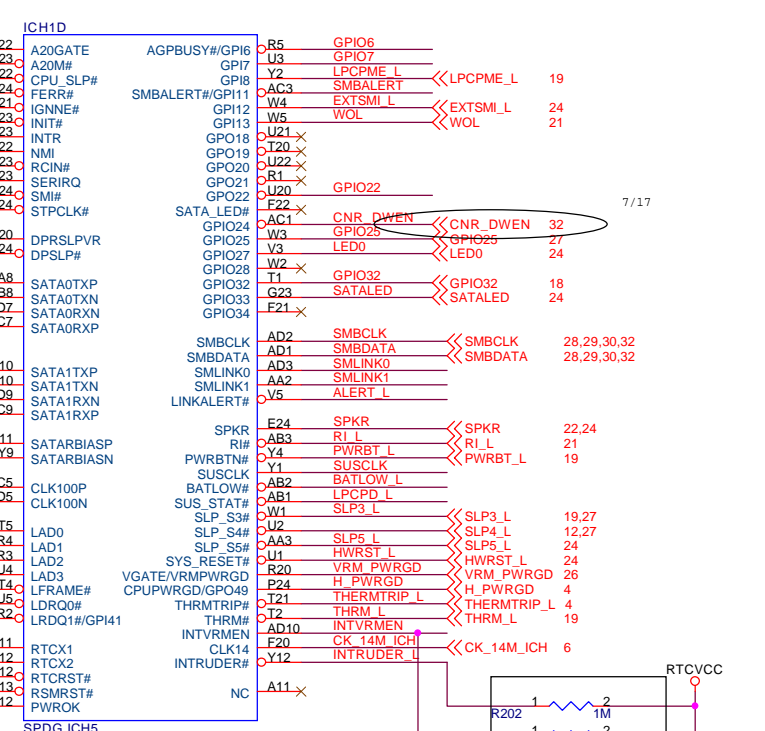
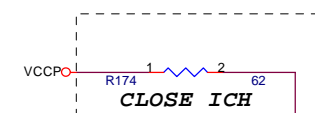
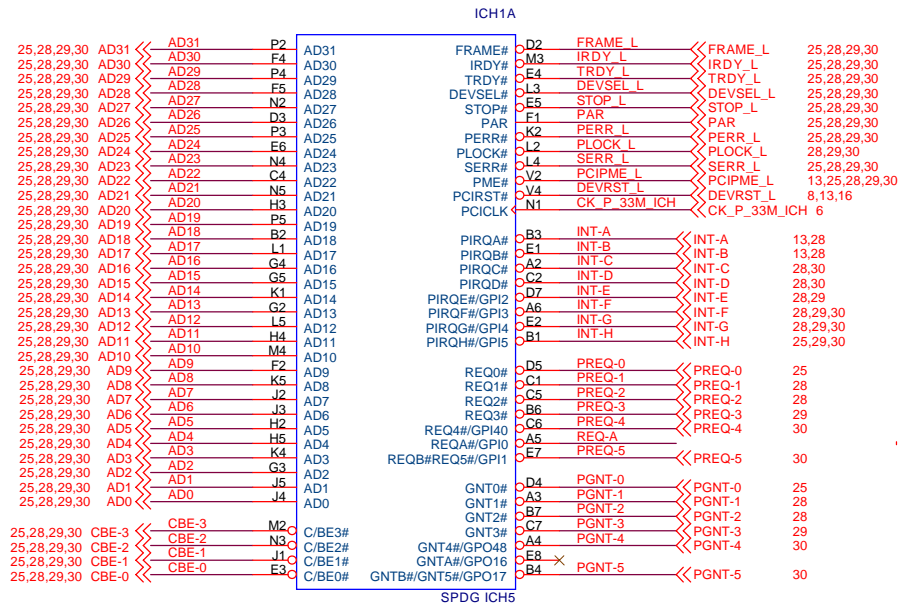
 Elitegroup Computer Systems		
Title DDIMM 3&4 (DDR SDRAMs)		
Size	Document Number	Rev
Custom	848P-A	2.0
Date:	Monday, December 29, 2003	Sheet 11 of 32



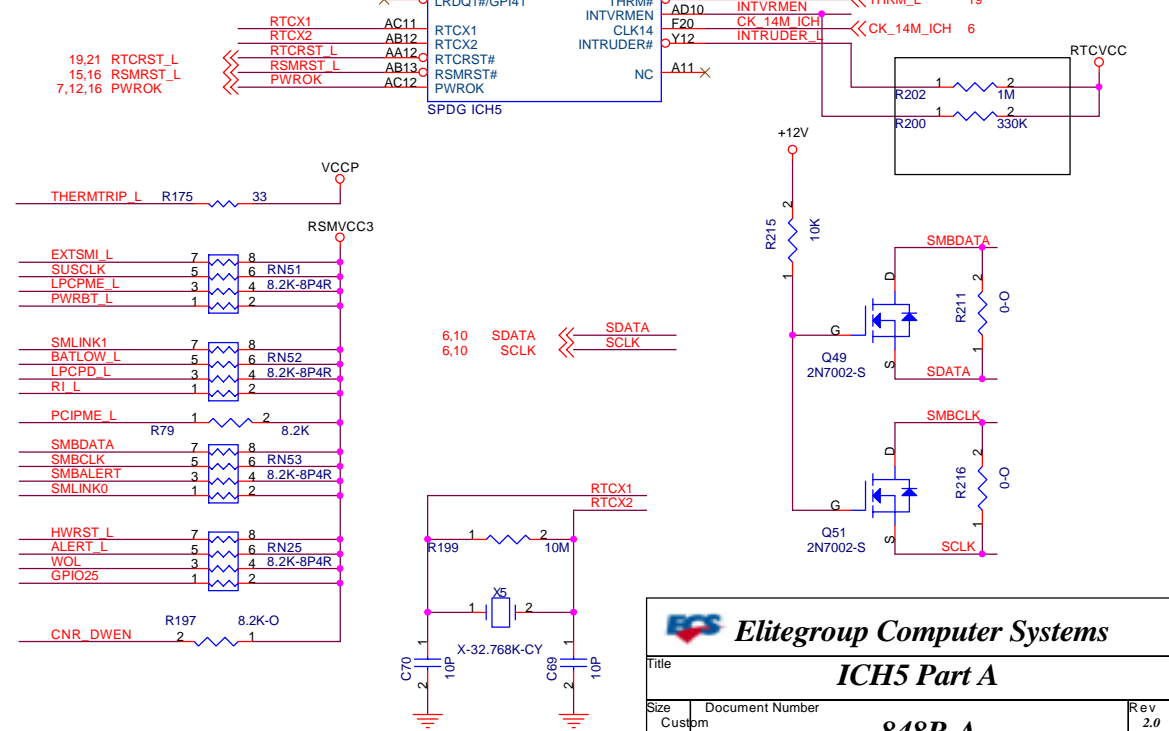
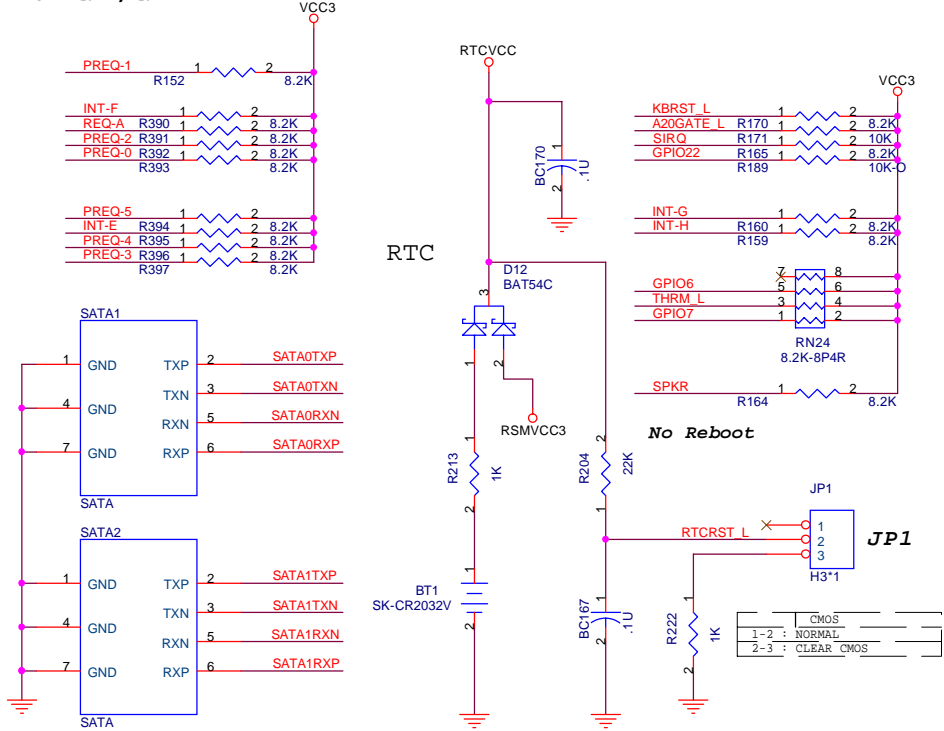
9173-SO8



NORTHWOOD : 1.45V
PRESCOTT : 1.25V

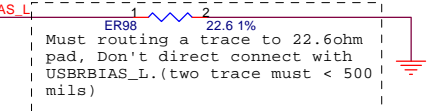
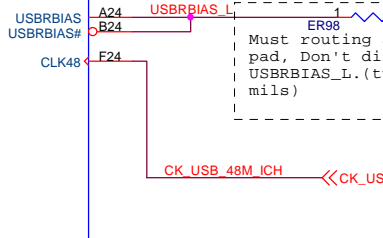
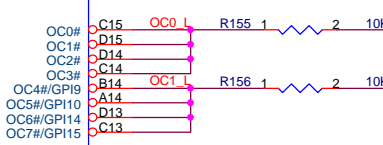
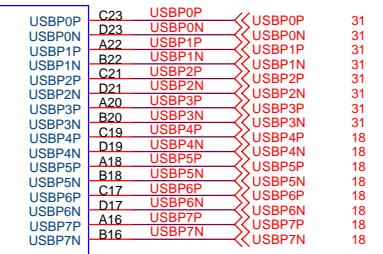
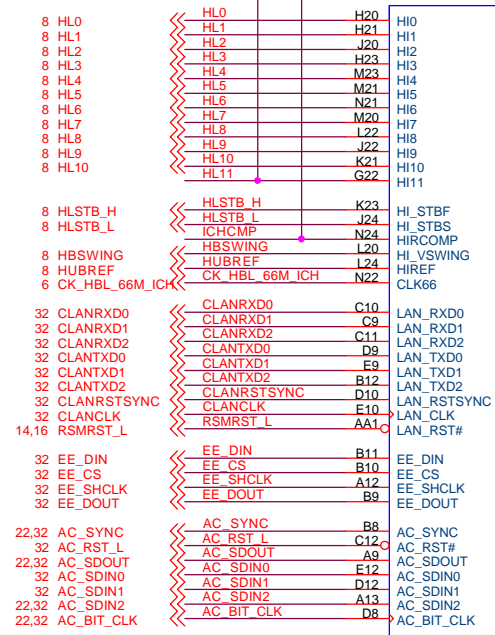
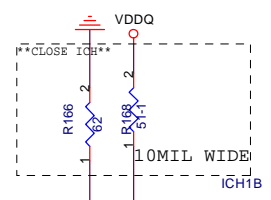


De1 RN22,RN21



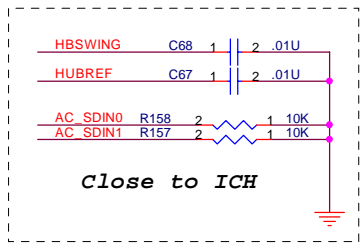
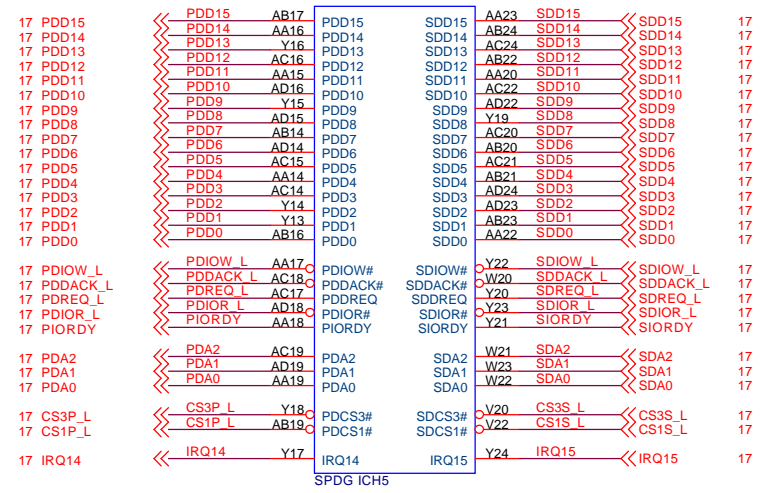
HL[0..10] << HL[0..10] 8

17 PDD[0..15] << PDD[0..15]
17 SDD[0..15] << SDD[0..15]

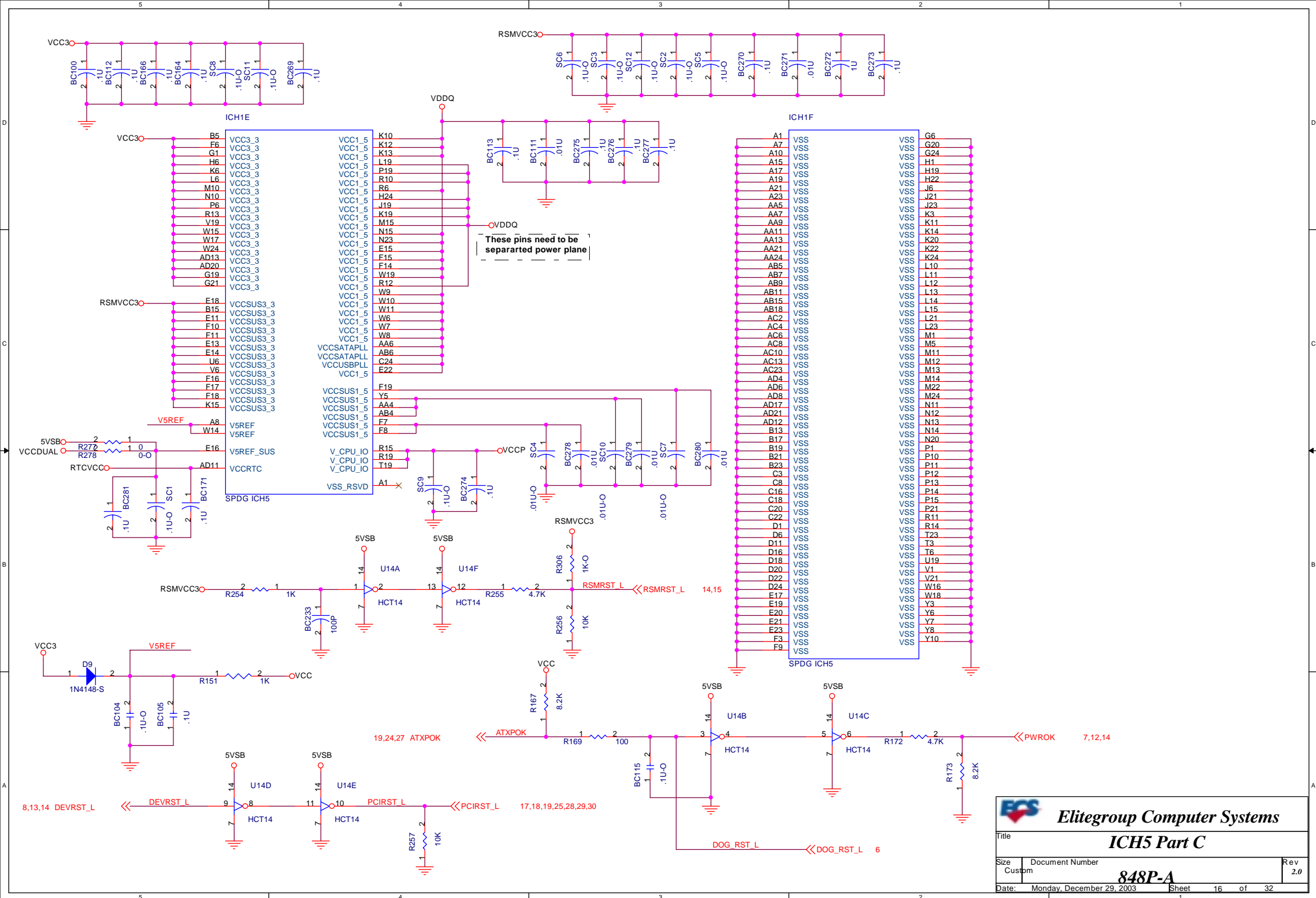


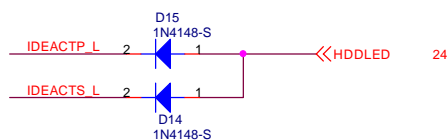
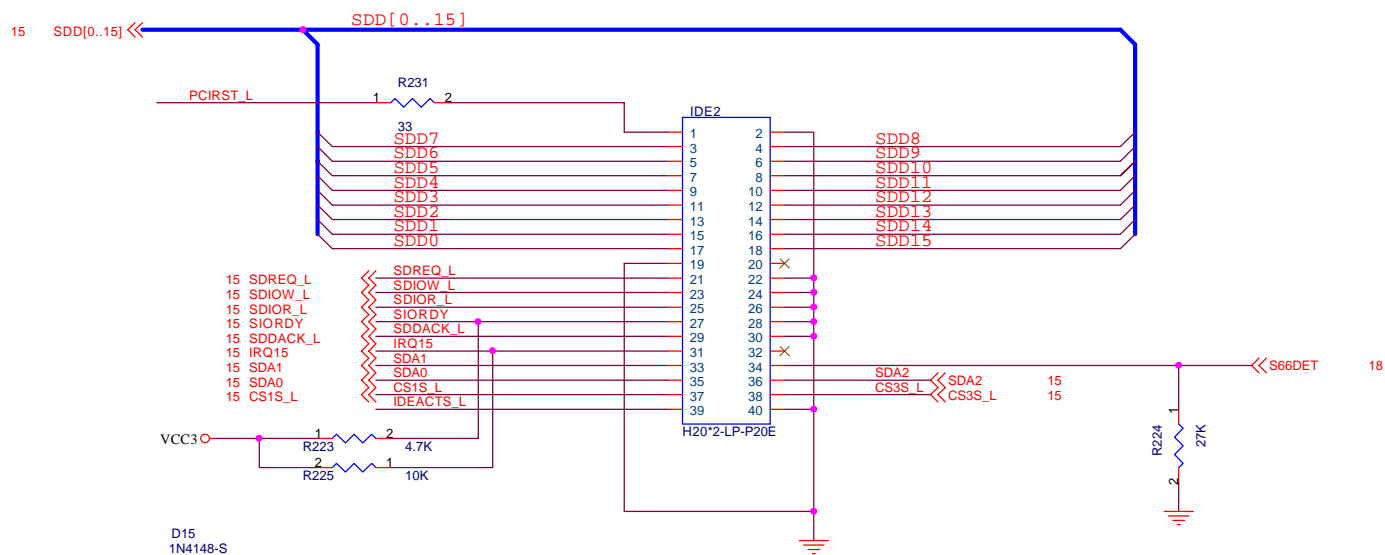
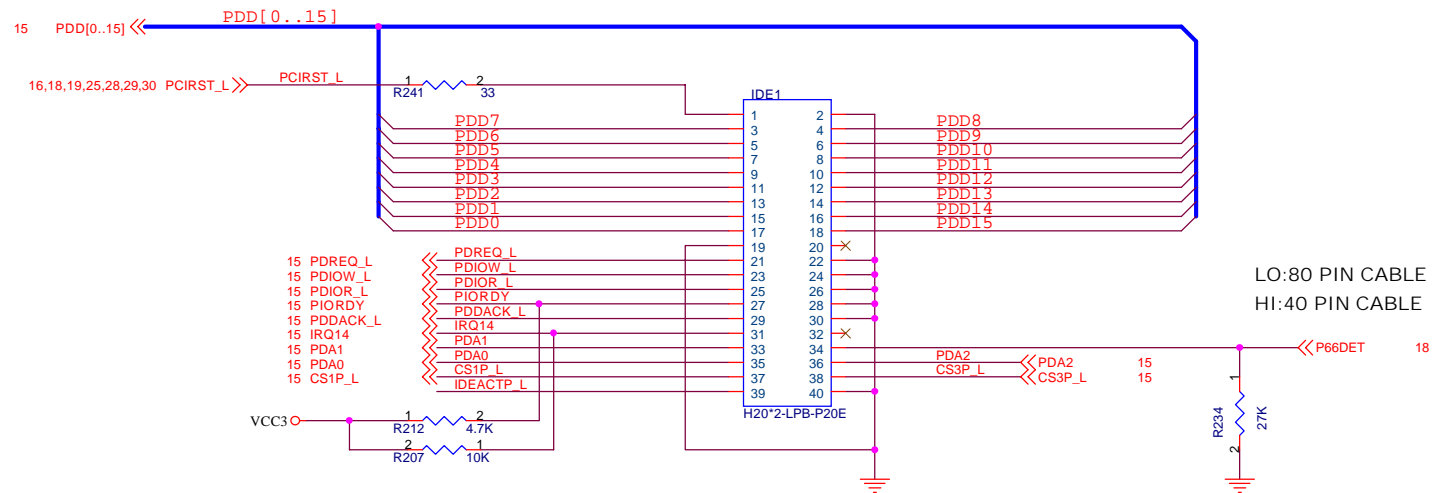
CK_USB_48M_ICK << CK_USB_48M_ICK 6

ICH1C



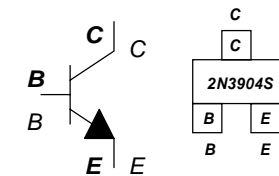
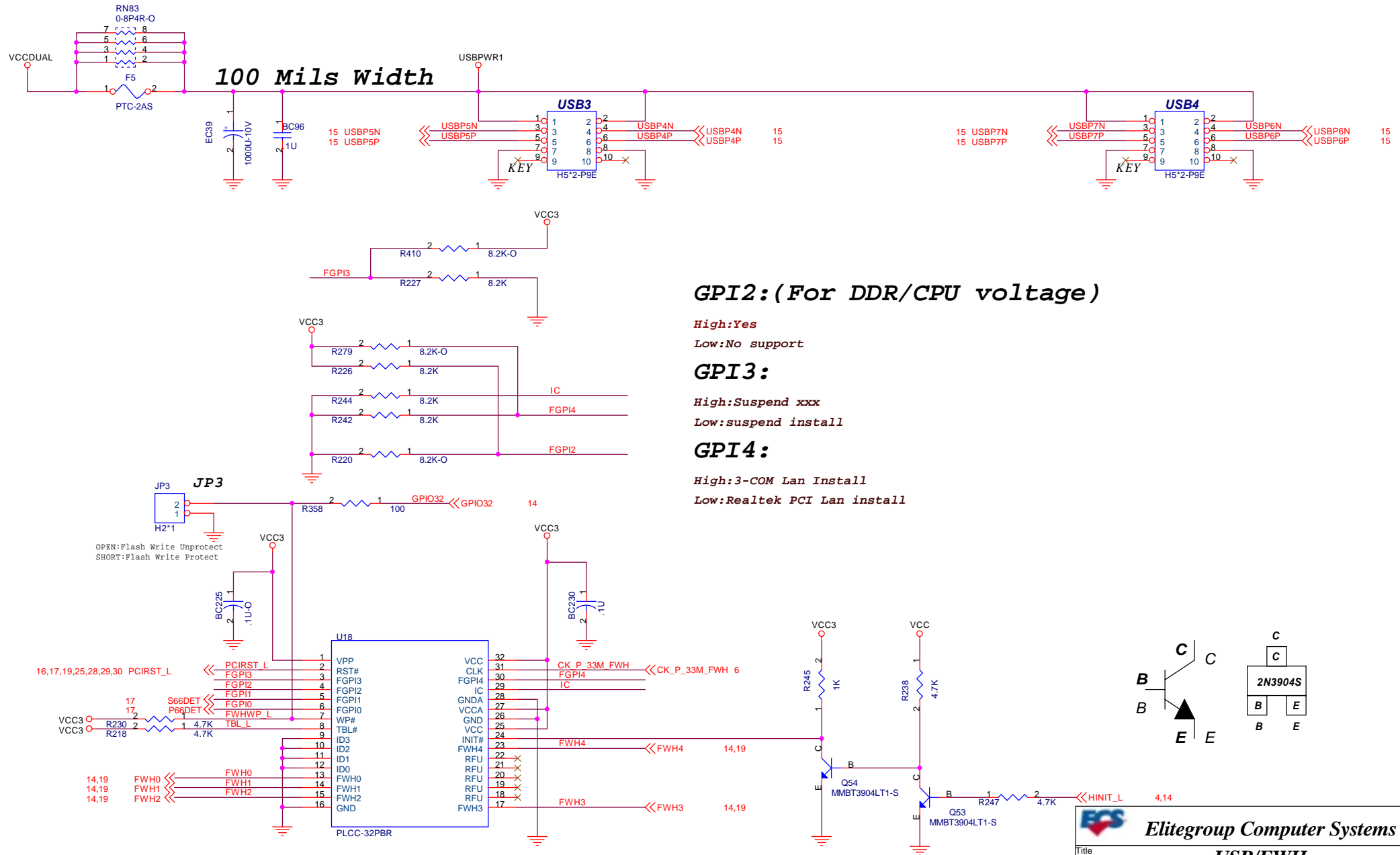
AC_SDOUT R46 2 1 8.2K-O VCC3
Safe Mode

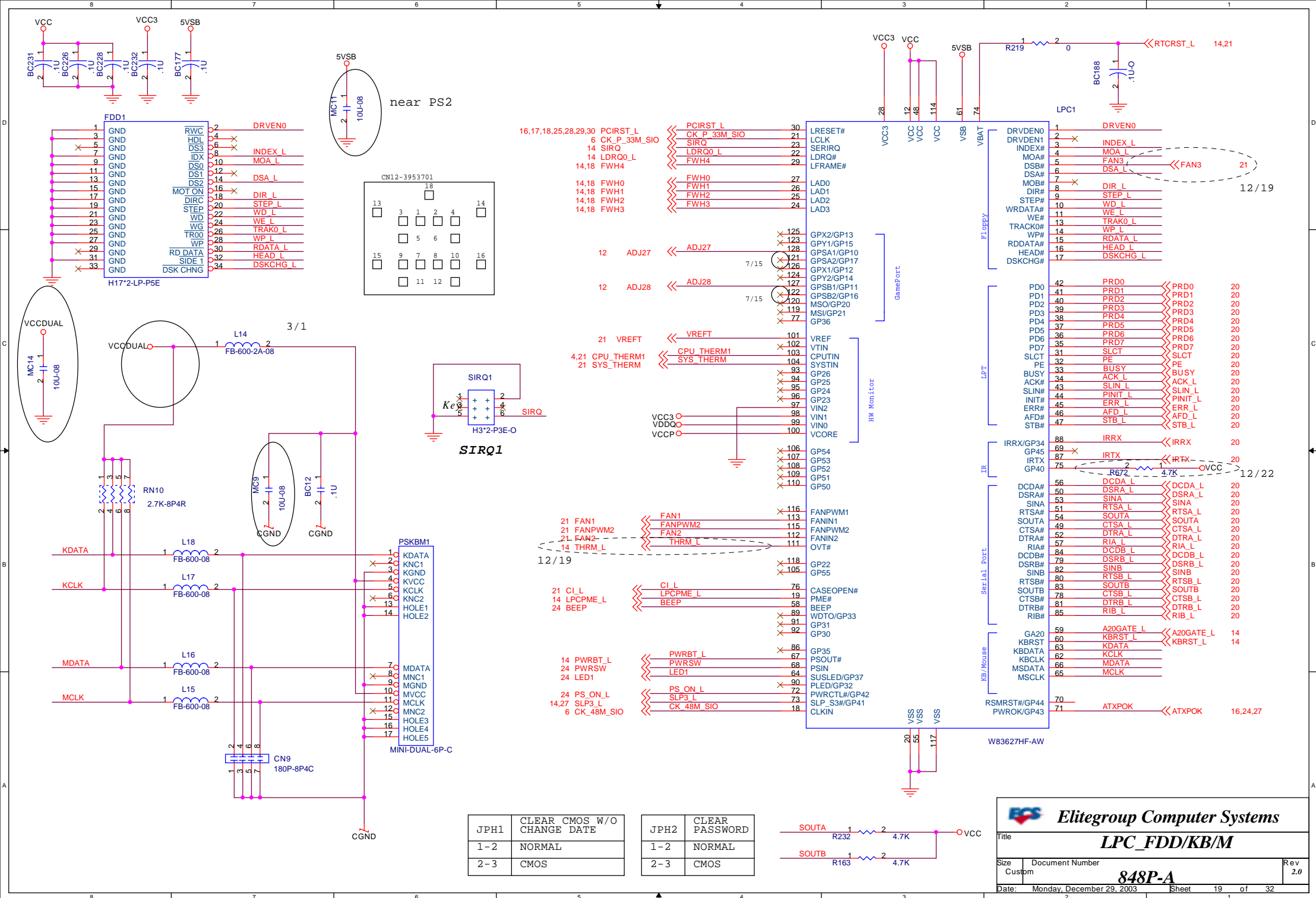


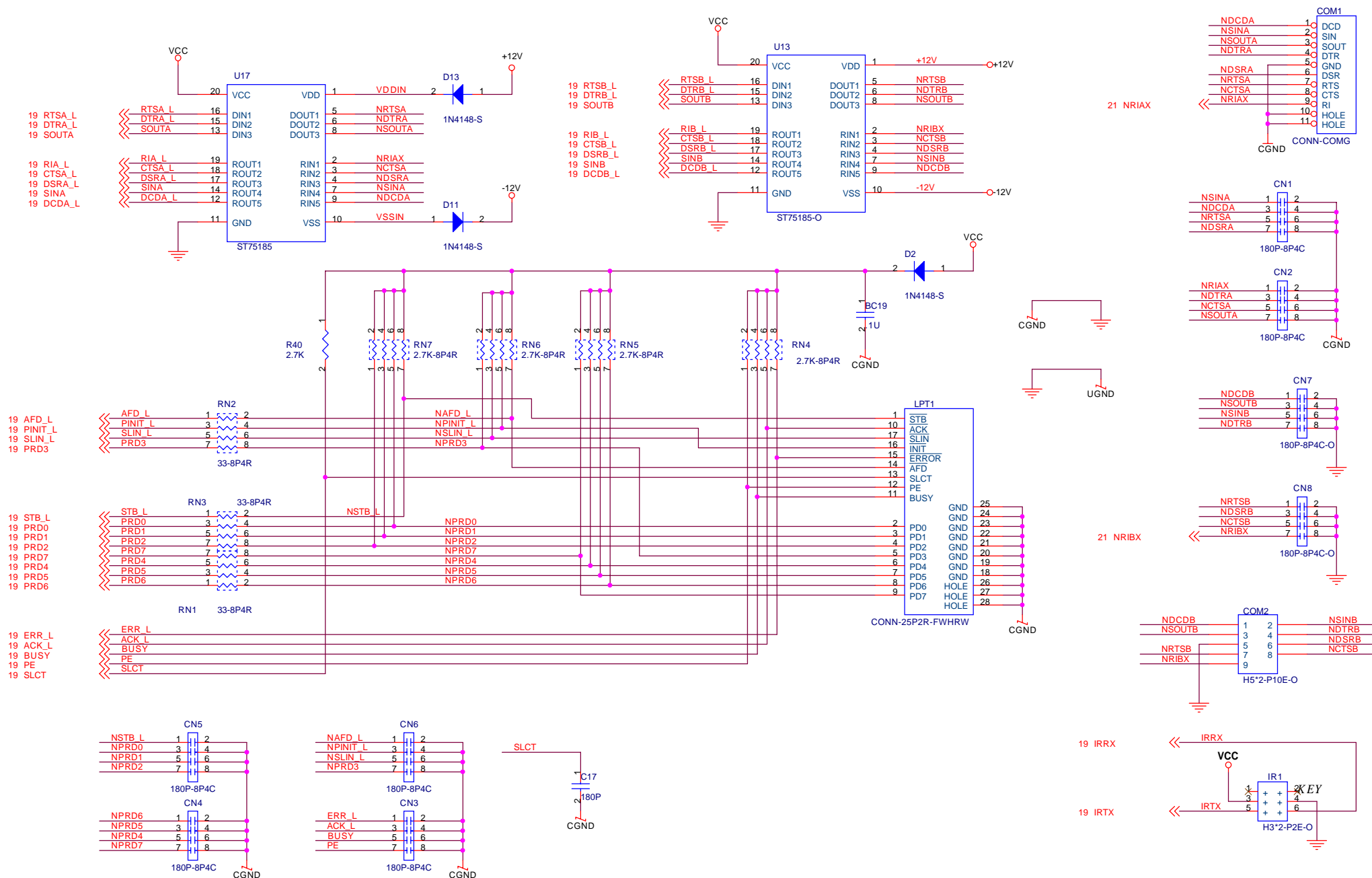


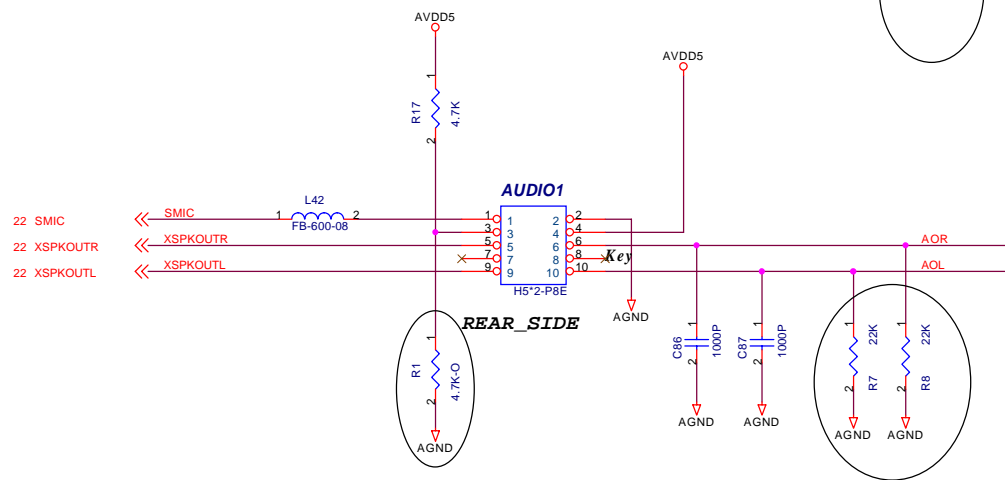
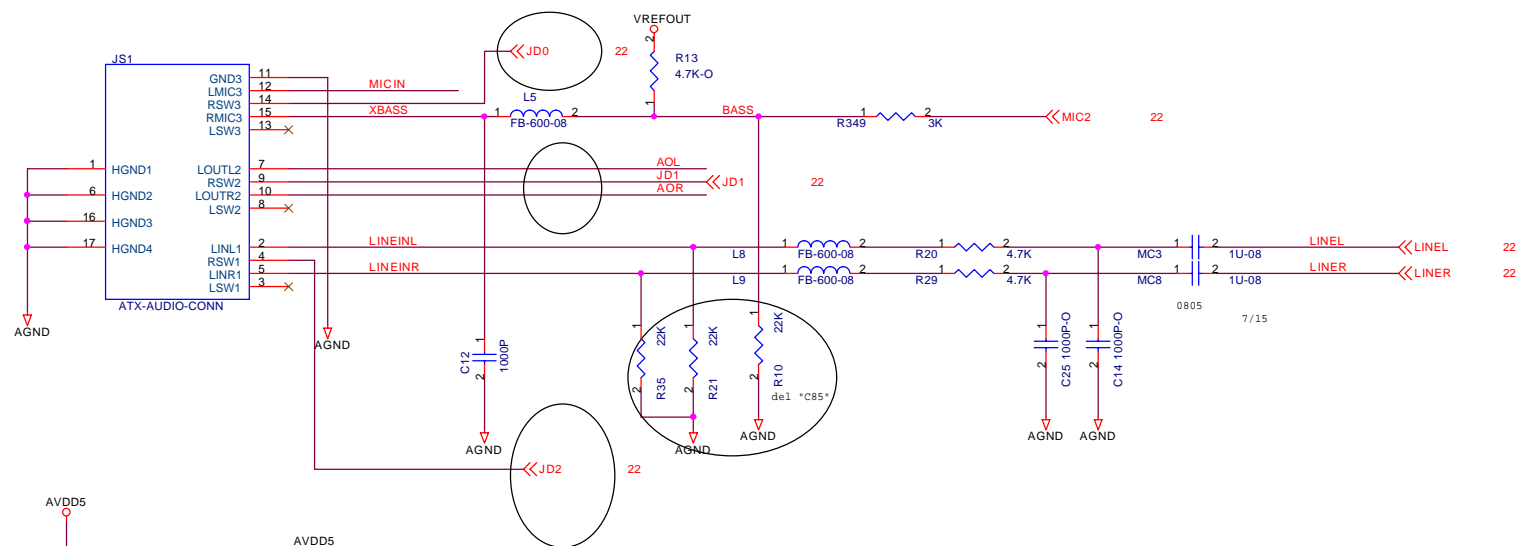
MAX TRACE LENGTH IS 8"

USB PORT INTERFACE

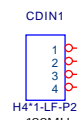




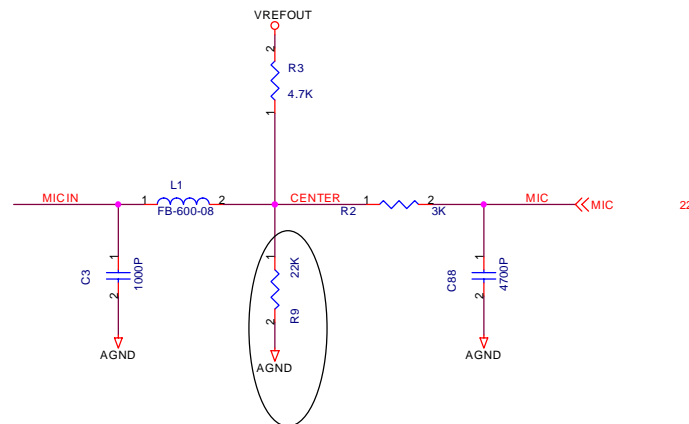
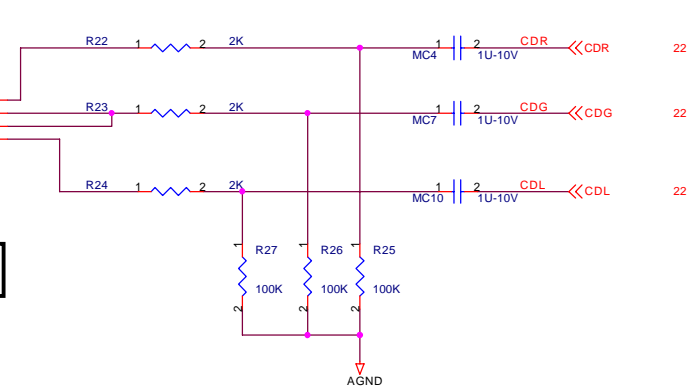


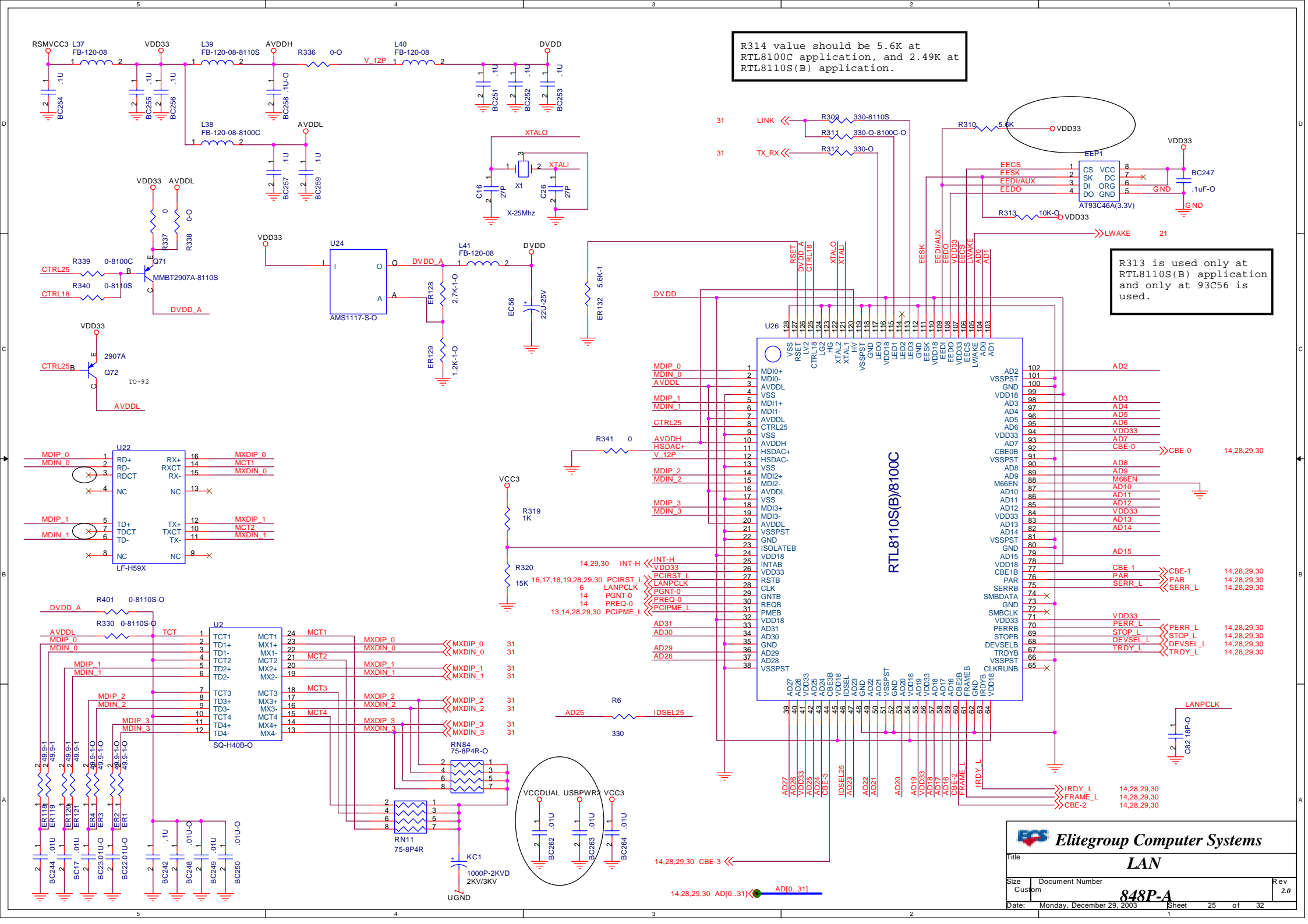


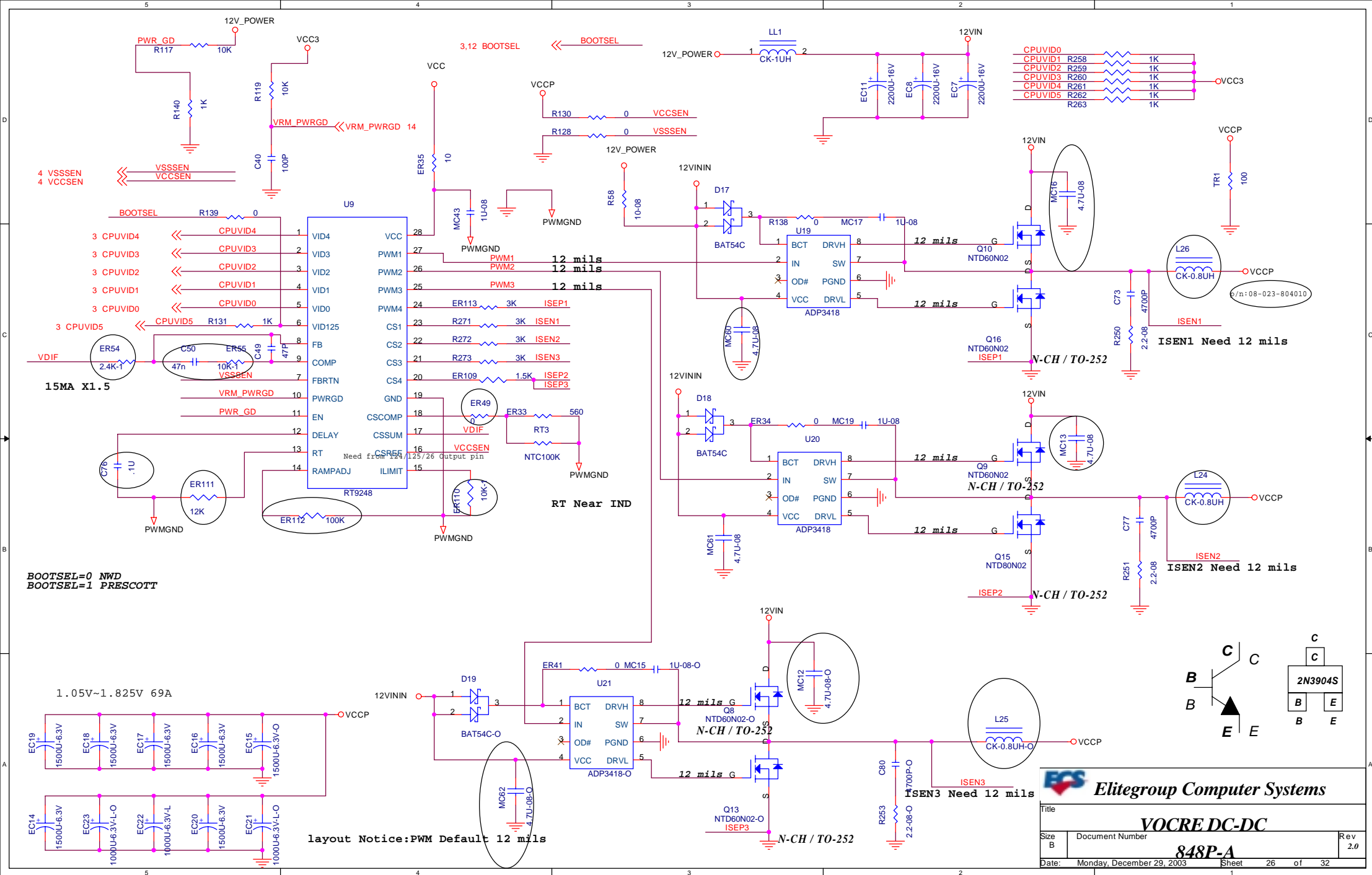
SONY
Big Black
10-341-004008
(2.5mm)

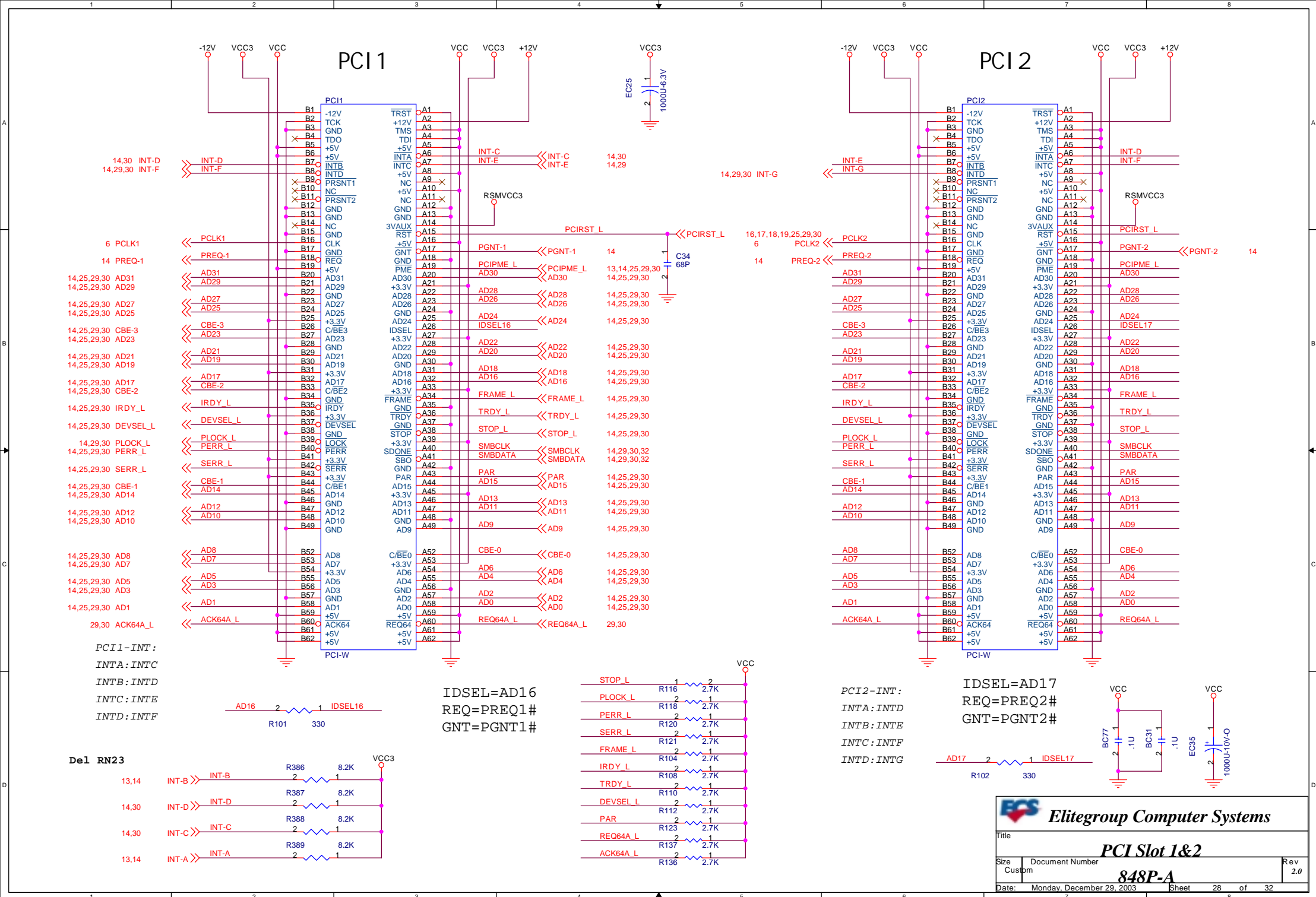


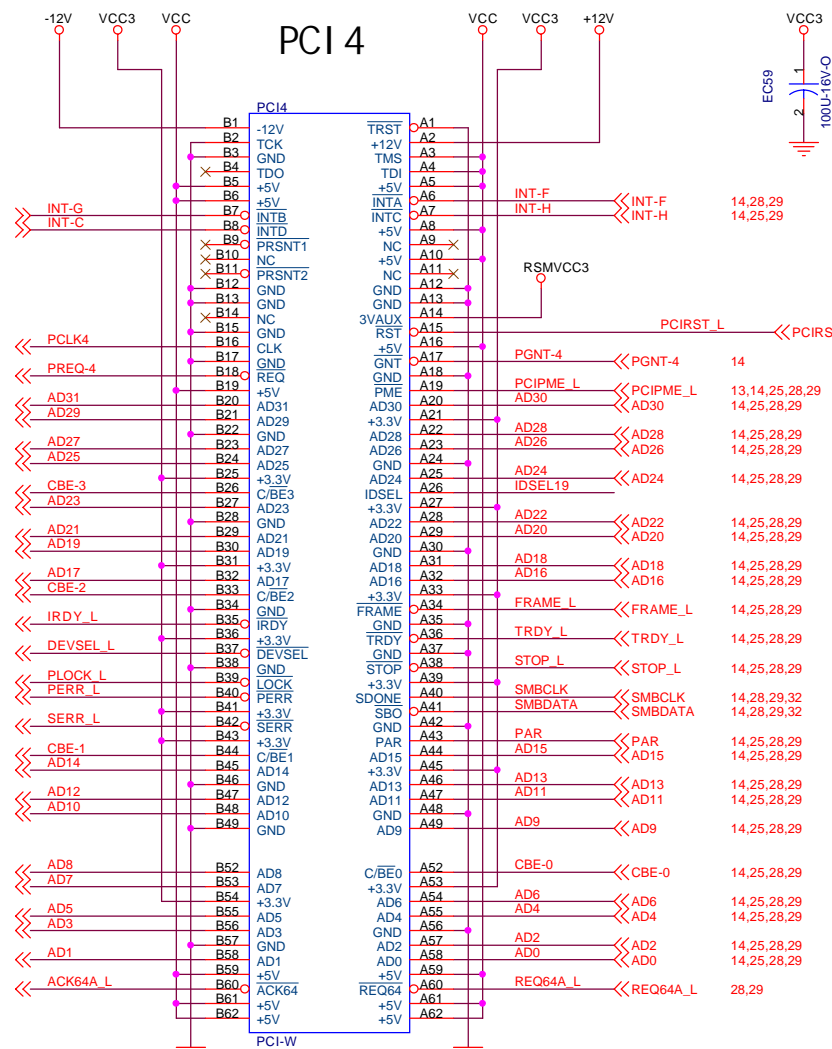
Top view





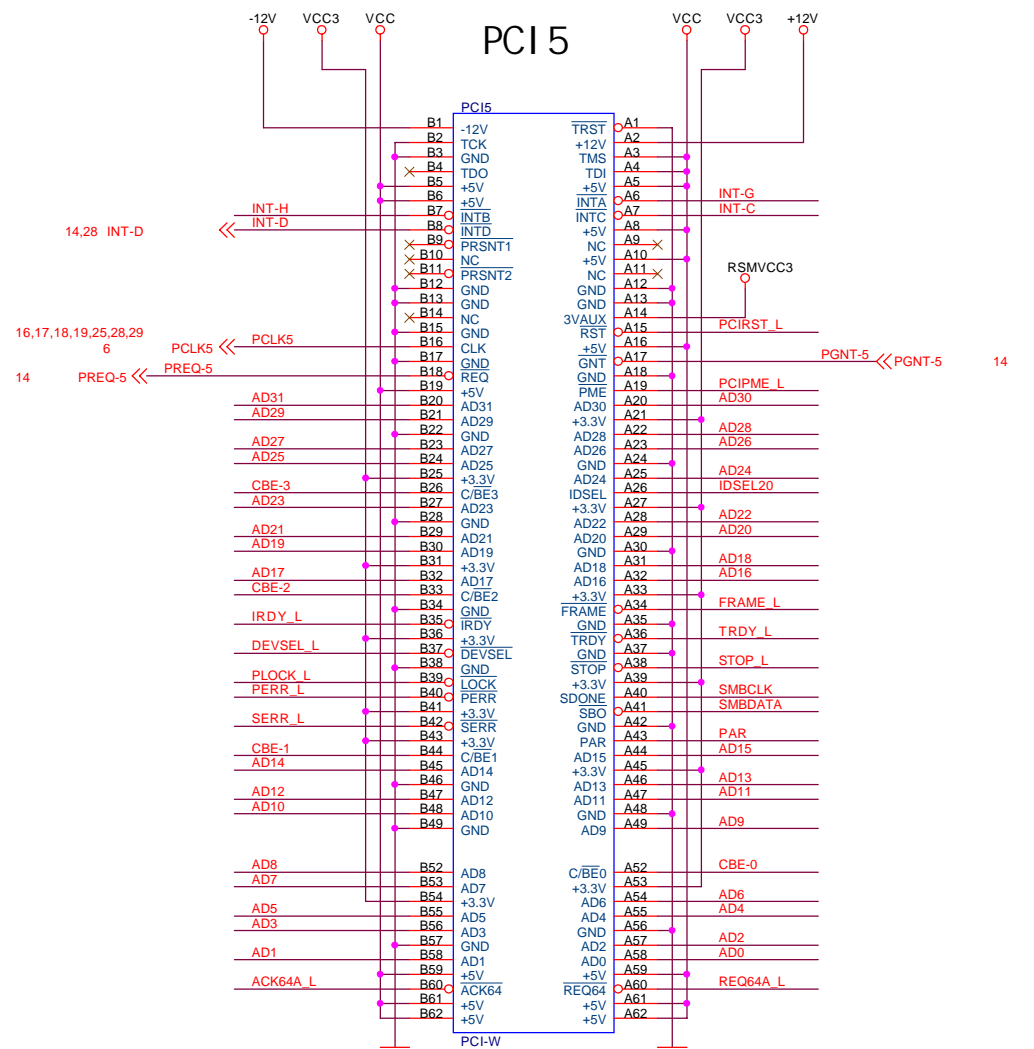
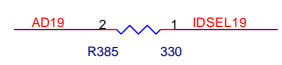






PCI4-INT :
INTA: INTF
INTB: INTG
INTC: INTH
INTD: INTD

IDSEL=AD19
REQ=PREQ4#
GNT=PGNT4#



PCI5-INT :
INTA: INTG
INTB: INTH
INTC: INTD
INTD: INTD

IDSEL=AD20
REQ=PREQ5#
GNT=PGNT5#

